from the coefficient control unit to the input luminance signal for outputting a contour-emphasized luminance signal.

11. A contour emphasizing circuit according to Claim 10, wherein the coefficient control unit comprises n number of multipliers for multiplying the contour component sampled by using the contour pick-up unit, the contour emphasizing coefficient corresponding to one of said n number of luminance levels for outputting the product thereof, n number of AND gates respectively connected to the output sides of the n number of multipliers for using the signal interpreted by said decoder as the gate control signal, and an OR gate connected to the output sides of the n number of AND gates.

Cl Cont

12. A contour emphasizing circuit according to Claim 11, wherein the decoder interprets whether the luminance level of an input video signal corresponds to which of four luminance levels, and the coefficient control unit comprises four multipliers for multiplying the contour components sampled by using the contour pick-up unit by any of coefficients 1/8, 1/4 1/2 and 1 for outputting the product thereof, four AND gates respectively connected to the output sides of the four multipliers for using the signal interpreted by said decoder as the gate control signal, the OR gate connected to the output sides of the four AND gates.

13. A contour emphasizing circuit according to Claim 10, wherein the contour pick-up unit comprises a horizontal contour component pick-up unit for sampling the contour component in horizontal direction from the input luminance signal.

14. A contour emphasizing circuit according to Claim 11, wherein the contour pick-up unit comprises a horizontal contour component pick-up unit for sampling the contour